

Notice of References Cited	Application/Control No. 10/713,971	Applicant(s)/Patent Under Reexamination IEONG ET AL.	
	Examiner Stanetta D. Isaac	Art Unit 2812	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-6,656,810	12-2003	Fukushima, Yasumori	438/320
	C	US-6,492,696	12-2002	Morimoto et al.	257/412
	D	US-6,060,749	05-2000	Wu, Shye-Lin	257/347
	E	US-6,429,055	08-2002	Oh, Jeong Hee	438/149
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Stanley Wolf and Richard N. Tauber, Silicon Processing For The VLSI Era, 2000, Lattice Press, Second Edition, pgs. 202, 421-423, 833-834
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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